

FOR PATENT APPLICATION (WITH OR ATTACHED)

As an inventor named below or on any attached continuation page, I hereby declare that:
 My residence, post office address and citizenship are as stated next to my name.
 I believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint Inventor (if plus names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled INTEGRATE CIRCUIT PACKAGE ELECTRICAL ENHANCEMENT, the specification of which (check one):

- is attached hereto.
 was filed on _____ as United States application serial no. _____ and was amended on _____
 was filed on _____ as PCT International application no. _____ and was amended under PCT Article 19 on _____

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as entered or amended referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, of Federal Regulations § 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor certificate or of any PCT International application(s) designating at least one country other than the United States of America listed below on any attached continuation page and have also identified below and on any attached continuation page any foreign application for patent or inventor's certificate or any PCT International application(s) designating at least one country other than the United States of America with filing date before that of the application(s) on which priority is claimed.

Prior foreign/PCT application(s):

			Priority Claim
(number)	(country)	(day/month/year filed)	Yes
_____	_____	_____	_____
_____	_____	_____	_____

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) or PCT International application(s) designating the United States of America listed below and on any attached continuation page and, insofar as the subject matter of each the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, 1.56(a) which occurred between the filing date of the prior application(s) and the national or PCT International filing date of this application.

(application serial no.)	(filing date)	(status - pending, patented or abandoned)
_____	_____	_____
_____	_____	_____

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

(provisional application no.)	(filing date)
_____	_____
_____	_____
_____	_____

I hereby appoint the following Registered Practitioners to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statement may jeopardize the validity of the application or any patent issued thereon.

Full name of first joint inventor: David J. Coria

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Date

9-4-96

DECLARATION FOR PATENT APPLICATION
(continuation page)

Invention title: INTEGRATED CIRCUIT PACKAGE ELECTRICAL ENHANCEMENT

Inventor name(s) appearing on first declaration page: David J. Corisia

Additional original, first and joint inventor(s):

Full name of second joint inventor: Jerry M. Brooks

Inventor's signature Jerry M. Brooks

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Date

9/10/96